

ABSTRACT OF THE DISCLOSURE

A method for activating n electrical loads in a circuit
assemblage, the activation being accomplished with pulse width
5 modulated signals. Both an inductance L influencing
electromagnetic compatibility and a capacitance C influencing
the same, with which a current I_L flowing in a supply lead can
be buffered, are provided. The n pulse width modulated signals
are generated in time-offset fashion so that the effective
10 value of a current I_L flowing in a supply lead to the N
electrical loads is reduced.